

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

JITTER-RESISTIVE DELAY LOCK LOOP CIRCUIT FOR
LOCKING DELAYED CLOCK AND METHOD THEREOF

Application Number :

Confirmation Number:

First Named Applicant: Jui-Hsing Tseng

Attorney Docket Number: MTKP0186USA

Art Unit:

Examiner:

Search string: (6069507).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6069507	2000-05-30	Shen et al.			

Signature

Examiner Name	Date